

**REMARKS**

Claims 1, 8, 10, 11, and 26 are pending in the present application. By virtue of this response, claim 1 has been amended. Accordingly, claims 1, 8, 10, 11, and 26 are currently under consideration. Amendment and cancellation of certain claims is not to be construed as a dedication to the public of any of the subject matter of the claims as previously presented. No new matter has been added.

Claim 1 has been amended to read "one of the N-type semiconductor layer and", as suggested by the Examiner in the final Office Action mailed on September 20, 2005 (Paper No. 20050916, page 2, fourth paragraph)

In view of the above, each of the presently pending claims in this application is believed to be in immediate condition for allowance. Accordingly, the Examiner is respectfully requested to withdraw the outstanding rejection of the claims and to pass this application to issue. If it is determined that a telephone conference would expedite the prosecution of this application, the Examiner is invited to telephone the undersigned at the number given below.

In the event the U.S. Patent and Trademark office determines that an extension and/or other relief is required, applicant petitions for any required relief including extensions of time and authorizes the Commissioner to charge the cost of such petitions and/or other fees due in connection with the filing of this document to Deposit Account No. 03-1952 referencing docket no. **259052002900**. However, the Commissioner is not authorized to charge the cost of the issue fee to the Deposit Account.

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Respectfully submitted,

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